A STRUCTURED DEVS MODEL REPRESENTATION BASED ON EXTENDED STRUCTURED MODELING

Yunping Hu
Jun Xiao
Gang Rong

Institute of Cyber-Systems and Control
State Key Laboratory of Industrial Control Technology, Zhejiang University
Hangzhou, Zhejiang 310027, P.R.C

Xiaolin Hu

Department of Computer Science
Georgia State University
Atlanta, GA 30303, USA

ABSTRACT
Developing a simulation model needs lots of costs. If the model elements can be reused in newly developed models of the same physical system, the modeling costs will be reduced. Traditional DEVS model representations depend on programming languages. A modeler is difficult to identify the DEVS semantics of model elements, which limits the reuse of existing models. In this paper, the structured modeling technology is used to represent DEVS models. A DEVS model is represented as a structured model. An atomic model can be represented as a genus graph and a modular tree, and a coupled model can be represented as elemental detailed tables. Based on the visual representation, models can be stored, maintained and reused easily. Two cases for the application of structured DEVS model representation are also presented.

1 INTRODUCTION
Simulation modeling is a process of abstraction with consideration of some objective. Reuse of existing models can significantly reduce the modeling costs and improve the quality of simulation when developing new models for the same physical system. If a model can be represented as a structured format, where the model elements and the relations between elements can be represented visually, the model reusability will be improved significantly.

In this paper, we focus on the reuse of existing models in the Discrete Event System Specification (DEVS) formalism (Zeigler, Praehofer, and Kim 2000), which has been used in discrete event simulation for more than 30 years. Traditional DEVS implementations, like DEVSJava (Sarjoughian and Zeigler 1998) and CD++ (Wainer 2002), are mainly developed in the object-orientation (OO) programming languages. An atomic DEVS model is usually represented by an OOP class, and the DEVS behaviors are modeled as methods of this class. The OO model representations have better usability and can be executed by simulators implemented in the same OO programming languages. However, it is difficult to identify the DEVS semantics of source codes in the programming environments, which influences the model reusability.
From the structured view, these model representations are structured with elements of package, class, attribute and method in the programming aspect. They are not structured in the DEVS aspect because their elements for representation have no clear DEVS semantics.

Recently, some new DEVS modeling tools, which try to represent models in methods independent on sources codes, have been developed. In Component-based System Modeler (CoSMo), a system is modeled through three model types: template models, instance template models and instance models (Sarjoughian and Elamvazhuthi 2009). CoSMo visualizes the model structure as a tree, where each node corresponds to a port, a model or a component. AutoDEVS uses constrained natural language (NL) to define FDDEVS models and then elaborate models using source codes (Salas and Zeigler 2009). For better mode compositionality, AutoDEVS uses system entity structure (SES) to represent the model structure. However, SES cannot describe the model behavior. From the structured view, CoSMo and AutoDEVS both have a structured representation for the model structure, while they do not support a structured model behavior. Their representations of the model behavior depend on sources codes or are limited by FDDEVS.

In this paper, we use Extended Structured Modeling (ESM) to represent a DEVS model as a structured model. For the atomic DEVS formalism, a specific ESM model schema for a general atomic model is proposed. For the coupled DEVS formalism, a specific ESM model schema for a coupled model class is proposed. In these two model schemas, a set of ESM genera and modules with clear DEVS semantics are defined. This model representation not only has a structured model structure, but also has a structured model behavior. Modelers can manage the model elements visually and can modify or reuse models conveniently.

The remaining of this paper is structured as follows. In Section 2, we introduce ESM as the background knowledge. Section 3 describes the structured DEVS model representation based on ESM in detail. Applications and benefits of this representation are shown in Section 4, and conclusions and future work are discussed in Section 5.

2 EXTENDED STRUCTURED MODELING

Structured Modeling (SM) is a kind of model representation based on discrete mathematics in the management science/operations research community (Geoffrion 1989). It uses a hierarchically organized, partitioned and attributed acyclic graph to represent a model. SM has five element types:

1. A primitive entity element is to represent any identifiable entity.
2. A compound entity element is a segmented tuple of primitive entity elements and/or other compound entity elements.
3. An attribute element, which may be constant or variable, is a segmented tuple of entity elements together with a value in some range.
4. A function element is a segmented tuple of elements together with a rule that calculate a particular value based on the attribute elements of the same tuple.
5. A test element is like a function element, except that its value is only true or false.

However, SM focuses on the representation of static models vis-a-vis dynamic models. ESM (Lenard 1992, Lenard 1993) extends SM for representing discrete event simulation (DES) models and proposes three new elements:

1. A random attribute element is to represent a random variable.
2. An action element is to represent a change in a model element.
3. A transaction element is a combination of action elements associated with a control structure.

The segmented tuple portion of an element is called its calling sequence. If one element appears in another calling sequence, the former is said to call the latter. ESM has been used to DES models in the event-oriented world view. Unfortunately, ESM has not been used to represent DEVS models.
3 DEVS MODEL REPRESENTATION BASED ON ESM

A structured model has two parts: a model schema and a model instance. A model instance is composed of elements and is represented as elemental tables. The elements with similarity can be treated as a genus. A model schema is composed of genera and is represented as a genus graph, where the genera are connected by calling sequences. A model schema can also be represented as a modular structure tree, where the rooted node and the intermediate nodes are modules and the leaves are genera. The structured DEVS model representation is developed based on ESM and the DEVS formalism, as shown in Figure 1. In this version, we focus on the Classic DEVS formalism.

![Figure 1: The framework of the DEVS representation based on ESM](image)

An atomic model uses a series of variables (input or output variable, state variable) and rules (state transition function, output function) to represent a physical system. The simulation process of an atomic model describes the physical system completely in a time period. This process of a model is different, if the initial states or input events are different. Therefore, in the ESM view, various simulation initial states, input sets and results of an atomic model are treated as model instances, while the atomic model is treated as a model schema.

Different atomic models in the DEVS formalism have some common features. In this paper, we develop an atomic model template which contains common genera for representing various atomic models. An atomic model of some physical system can be represented according to the template. The model instance of an atomic model is to represent the event-driven simulation behavior.

A coupled DEVS model has no model behavior. In the genus and modular level, various coupled model share the same model schema. Using ESM, we develop a specific model schema for representing the coupled model class. A coupled model of some physical system is only an instance of this model schema.

In the following, for describing the model schemas, three formats are used: Structuring Modeling Language (SML), genus graph and modular tree. SML is a kind of textual modeling language to represent a model schema in detail(Geoffrion 1992a, Geoffrion 1992b). The model instances are represented as elemental tables.

3.1 Atomic DEVS Model Representation Based on ESM

For representing the atomic DEVS model template, the SML schema is shown in Figure 2. It explains the modules and genera of the atomic DEVS model template in detail.

The simulation process of an atomic DEVS model is event-driven. For describing the dynamic behavior, an EVENT primitive set is defined. Each event occurs in some time point and may cause the update of state variables. So each EVENT element has attributes of TIME VALUE, TIME ELAPSED VALUE, CURRENT STATE, CURRENT PHASE, CURRENT TIME ADVANCE, NEXT STATE, NEXT PHASE...
There is a list of EVENTS.

**EVENTe** /pe/ There is a list of EVENTS.

- **TIME_VAL (EVENTe)** /va/ : Int Every EVENT point has a TIME VALUE.
- **E_VAL (EX_EVENTe)** /va/ : Int Every EVENT point has a TIME ELAPSED VALUE.
- **CUR_STATE (EVENTe)** /va/ : Int Every EVENT point has a CURRENT STATE.
- **NXT_STATE (EVENTe)** /va/ : Int Every EVENT point has a NEXT STATE.
- **CUR_PHASE (EVENTe)** /va/ : Int Every EVENT point has a CURRENT PHASE.
- **NXT_PHASE (EVENTe)** /va/ : Int Every EVENT point has a NEXT PHASE.
- **CUR_SIGMA (EVENTe)** /va/ : Int Every EVENT point has a CURRENT TIME ADVANCE.
- **NXT_SIGMA (EVENTe)** /va/ : Int Every EVENT point has a NEXT TIME ADVANCE.

**EXTERNAL_TRANSITION** module

**EXTERNAL_PORT** /pe/ There is a list of INPUT PORTs.

- **INPUT_PORT** /pe/ There is a list of INPUT PORTs.
- **INPUT_VAL (EX_EVENTe, INPUT_PORTip)** /va/ {EX_EVENT} Every EXTERNAL EVENT has an INPUT VALUE.
- **EX_TRANS (INPUT_PORTip) /tr/ {EX_EVENT} An EXTERNAL EVENT may result an EXTERNAL TRANSITION transaction which may call EX_STATE_UPD, EX_TEST in the same EX_EVENT point.

**INPUT_PORTIP** /pe/ There is a list of INPUT PORTs.

- **INPUT_PORT** /pe/ There is a list of INPUT PORTs.
- **INPUT_VAL (EX_EVENTe, INPUT_PORTip)** /va/ {EX_EVENT} Every EXTERNAL EVENT has an INPUT VALUE.
- **EX_TRANS (INPUT_PORTip) /tr/ {EX_EVENT} An EXTERNAL EVENT may result an EXTERNAL TRANSITION transaction which may call EX_STATE_UPD, EX_TEST in the same EX_EVENT point.

**INTERNAL_TRANSITION** module

**INTERNAL_PORT** /pe/ There is a list of OUTPUT PORTs.

- **OUTPUT_PORT** /pe/ There is a list of OUTPUT PORTs.
- **OUTPUT_VAL (OUTPUT_EVENTe,op)** /va/ {OUTPUT_EVENT} Every OUTPUT EVENT has an OUTPUT VALUE.
- **OUTPUT_TRANS (OUTPUT_PORTop) /tr/ {OUTPUT_EVENT} A INTERNAL EVENT may result an OUTPUT FUNCTION which may call OUTPUT_STATE_TEST and OUTPUT_ASS in the same OUTPUT_EVENT point.

**OUTPUT_PORTOP** /pe/ There is a list of OUTPUT PORTs.

- **OUTPUT_PORT** /pe/ There is a list of OUTPUT PORTs.
- **OUTPUT_VAL (OUTPUT_EVENTe,op)** /va/ {OUTPUT_EVENT} Every OUTPUT EVENT has an OUTPUT VALUE.
- **OUTPUT_TRANS (OUTPUT_PORTop) /tr/ {OUTPUT_EVENT} A INTERNAL EVENT may result an OUTPUT FUNCTION which may call OUTPUT_STATE_TEST and OUTPUT_ASS in the same OUTPUT_EVENT point.

**OUTPUT_PORTOP** /pe/ There is a list of OUTPUT PORTs.

- **OUTPUT_PORT** /pe/ There is a list of OUTPUT PORTs.
- **OUTPUT_VAL (OUTPUT_EVENTe,op)** /va/ {OUTPUT_EVENT} Every OUTPUT EVENT has an OUTPUT VALUE.
- **OUTPUT_TRANS (OUTPUT_PORTop) /tr/ {OUTPUT_EVENT} A INTERNAL EVENT may result an OUTPUT FUNCTION which may call OUTPUT_STATE_TEST and OUTPUT_ASS in the same OUTPUT_EVENT point.

**OUTPUT_PORTOP** /pe/ There is a list of OUTPUT PORTs.

- **OUTPUT_PORT** /pe/ There is a list of OUTPUT PORTs.
- **OUTPUT_VAL (OUTPUT_EVENTe,op)** /va/ {OUTPUT_EVENT} Every OUTPUT EVENT has an OUTPUT VALUE.
- **OUTPUT_TRANS (OUTPUT_PORTop) /tr/ {OUTPUT_EVENT} A INTERNAL EVENT may result an OUTPUT FUNCTION which may call OUTPUT_STATE_TEST and OUTPUT_ASS in the same OUTPUT_EVENT point.

Figure 2: SML schema for the atomic DEVS model template
and NEXT TIME ADVANCE. The phase variable and the time advance variable are inherent state variables in each atomic model.

All events in a simulation are divided into external events and internal events. When an input comes into a model from outside, an external event occurs and the external transition function is called to update the state variables based on the present state, the input value and the time elapsed in the current state. Therefore, we define an EXTERNAL EVENT set, which is a compound set combining the EVENT set with the INPUT PORT set. Each EXTERNAL EVENT has the attributes of INPUT VALUE, actions of EXTERNAL STATE UPDATE and tests of EXTERNAL TEST. The EXTERNAL TRANSITION transaction calls these actions and tests when an external event occurs.

When the elapsed time equals to the time advance in the current state, an internal event occurs and the internal transition function is called to update the state variables based on the present state. We define an INTERNAL EVENT set, a subset of the EVENT set. An INTERNAL EVENT may have INTERNAL STATE UPDATE actions and the INTERNAL CURRENT STATE TESTs. The INTERNAL TRANSITION transaction calls these actions and tests when an internal event occurs.

When an internal event occurs, the output function will be called to send out message before the internal transition function is called. So, each internal event is also an output event which associates with an output port. We define an OUTPUT EVENT set which is a compound set combining the INTERNAL EVENT set with the OUTPUT PORT set. Each OUTPUT EVENT has an attribute of OUTPUT VALUE and an OUTPUT ASSIGNMENT action and may have an OUTPUT CURRENT STATE TEST. The OUTPUT FUNCTION calls the action and the test when an output event occurs.

In classic ESM, each attribute has a value range in consideration of the mathematical aspect. However, each attribute here has a simple or complex data type. Because the DEVS implementations depend on programming languages, the data type property is necessary for an attribute.

For visualizing the atomic DEVS model template, the genus graph is shown in Figure 3. and the modular tree is shown in Figure 4. In this genus graph, the genera are connected as a directed graph through calling sequences. In this modular tree, the rooted node is the ATOMIC MODEL module and the intermediate notes have four module types: STATE, EXTERNAL_TRANSITION, INTERNAL_TRANSITION and OUTPUT.

![Figure 3: Directed genus graph for the atomic DEVS model template](image)

Furthermore, the modular tree here explains how to represent an atomic model of some physical system based on the model template. A specific atomic model may have other state variable in addition to the phase variable and the time advance variable. So in the STATE module, specific CUR_STATE attributes and NXT_STATE attributes may be added. Each EXTERNAL_TRANSITION module corresponds to an input port. The reason is that an atomic model may have different rules (actions and transactions) to change the state for different input ports. If an atomic model has more than one input ports, more than one EXTERNAL_TRANSITION modules are needed. The OUTPUT module is similar with the EXTERNAL_TRANSITION module and each OUTPUT module corresponds to only one output port.
A model instance of the model schema for the atomic model template can be represented as elemental detail tables, as shown in Table 1. These tables only contain entities and value-based elements including primitive entities, compound entities, attributes and tests. The column names of these tables come from the model schema for the atomic model template. For an atomic model of some physical system, these elemental tables may be extended in terms of the extension of the model template.

### Table 1: Elemental detail tables for the model instance of the atomic DEVS model template

<table>
<thead>
<tr>
<th>Table Name</th>
<th>Column Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>EVENT</td>
<td>EVENT</td>
</tr>
<tr>
<td>INPUT_PORT</td>
<td>INPUT_PORT</td>
</tr>
<tr>
<td>EX_EVENT</td>
<td>EVENT INPUT_PORT</td>
</tr>
<tr>
<td>IN_EVENT</td>
<td>EVENT</td>
</tr>
<tr>
<td>OUT_PORT</td>
<td>OUTPUT_PORT</td>
</tr>
<tr>
<td>OUTPUT_EVENT</td>
<td>EVENT OUTPUT_PORT</td>
</tr>
</tbody>
</table>

### 3.2 Coupled DEVS Model Representation Based on ESM

A coupled DEVS model only has elements of model structure. We define a series of primitive sets and compound sets in the model schema for the coupled DEVS model class. The SML representation of the coupled model class is shown in Figure 5.

A coupled model is composed of components, each of which associates with an atomic or coupled model. So, we define two primitive sets of MODEL and COMPONENT and a compound MODEL COMPONENT set. The ATOMIC MODEL set and the COUPLED MODEL set are both compound sets, subsets of the MODEL set. The components in a coupled model are connected by couplings: internal couplings (ICs), external input couplings (EICs) and external output couplings (EOCs). Therefore, we define three compound sets of IC, EIC and EOC. The select function can be represented as a set of ordered pairs, each element in which has one prioritized component and one or more than one concurrent components. So, we define a SELECT compound set which has two member COMPONENT sets.
There is a list of models.

A model has zero or more input ports.

There is a list of coupled models, a subset of models.

A model has zero or more output ports.

There is a list of atomic models, a subset of models.

A model component means that a component associates with a model.

A external output coupling represents an output coupling between a component and a coupled model.

A external input coupling represents an input coupling between a component and a coupled model.

A internal coupling represents an internal coupling between a component and another component.

A select function has a set of ordered pairs where the first elements are prioritized components and the second elements are concurrent components.

Figure 5: SML schema for the coupled DEVS model class

Based on the SML schema for the coupled DEVS model class, the genus graph is shown in Figure 6 and the modular tree is shown in Figure 7. The rooted node of the modular tree is COUPLED_MODEL, which has three intermediate nodes: MODEL, COMPONENT and COUPLING. The elemental detail tables for a coupled model are shown in Table 2. Modelers can write the coupled model elements for some physical system into these tables.

Figure 6: Directed genus graph for the coupled DEVS model class

4 CASES

Based on the structured DEVS model representation, we can represent specific DEVS models. In this section, we use two cases to test the model representation and discuss the benefits using this method.

4.1 The Structured Representation of an Atomic Model

We have defined an atomic model template based on ESM. In this section, we use the template to represent the processor model. The formal specification of the processor model is shown in Figure 8.

The modular tree and the genus graph for the processor model is shown in Figure 9. There is a state variable named job in the processor model. Therefore, in the state module, specific current...
Table 2: Elemental detail tables for the model instance of the atomic DEVS model template

<table>
<thead>
<tr>
<th>Table Name</th>
<th>Column Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>MODEL</td>
<td>MODEL</td>
</tr>
<tr>
<td>A_MODEL</td>
<td>MODEL</td>
</tr>
<tr>
<td>C_MODEL</td>
<td>MODEL</td>
</tr>
<tr>
<td>INPUT_PORT</td>
<td>INPUT_PORT</td>
</tr>
<tr>
<td>MODEL_INPUT_PORT</td>
<td>MODEL_INPUT_PORT</td>
</tr>
<tr>
<td>OUTPUT_PORT</td>
<td>OUTPUT_PORT</td>
</tr>
<tr>
<td>MODEL_OUTPUT_PORT</td>
<td>MODEL_OUTPUT_PORT</td>
</tr>
<tr>
<td>COMPONENT</td>
<td>COMPONENT</td>
</tr>
<tr>
<td>EIC</td>
<td>MODEL_INPUT_PORT COMPONENT INPUT_PORT</td>
</tr>
<tr>
<td>EOC</td>
<td>COMPONENT OUTPUT_PORT COMPONENT OUTPUT_PORT</td>
</tr>
<tr>
<td>IC</td>
<td>COMPONENT OUTPUT_PORT COMPONENT INPUT_PORT</td>
</tr>
<tr>
<td>SEL</td>
<td>COMPONENT COMPONENT</td>
</tr>
</tbody>
</table>

\[ \text{Processor} = \langle X, Y, S, \delta_{\text{ext}}, \delta_{\text{int}}, \lambda, \tau \rangle \]

Where

\[ \text{InPorts} = \{ \text{"in"} \}, \text{where } X_{\text{in}} = R_{0}^{+} \]

\[ X = \{(p,v) | p \in \text{InPorts}, v \in X_{p}\} \text{ is the set of input ports and values} \]

\[ \text{OutPorts} = \{ \text{"out"} \}, \text{where } X_{\text{out}} = R_{0}^{+} \]

\[ Y = \{(p,v) | p \in \text{OutPorts}, v \in Y_{p}\} \text{ is the set of Output ports and values} \]

\[ S = \{\text{phase, sigma, job}\} = \{\text{"passive", "active"}\} \times R_{0}^{+} \times R_{0}^{+} \]

\[ \delta_{\text{ext}}( \text{msg: type ExternalMessage} ) \{ \]

\[ \text{cur_job = job; cur_phase = phase; cur_sigma = sigma; } \]

\[ \text{if( msg.port == in ) } \{ \text{inJob = msg.value; job = inJob; } \]

\[ \text{if( cur_phase == passive ) } \{ \text{phase = active; sigma = 5; } \]

\[ \text{else sigma = cur_sigma - e; } \}

\[ \delta_{\text{int}}( \text{msg: type InternalMessage} ) \{ \]

\[ \text{cur_phase = phase; cur_sigma = sigma; } \]

\[ \text{phase = passive; sigma = Inf; } \}

\[ \lambda( \text{msg: type InternalMessage} ) \{ \]

\[ \text{cur_job = job; } \]

\[ \text{outJob = cur_job; send outJob to out port; } \}

\[ \tau(\text{"passive"}) = \text{Inf} \quad \tau(\text{"active"}) = 5 \]

Figure 7: Modular tree for the coupled DEVS model class

Figure 8: The formal specification of the processor model
STATE named cur_job and specific NEXT STATE named job are defined. Because there is only one input port and one output port, there is one EXTERNAL_TRANSITION module and one OUTPUT module in the modular tree. In the EXTERNAL_TRANSITION module, the tr1 transaction genus calls the phase_t test, the job_ac1 action, the sigma_ac1 action and the sigma_ac3 action with the if-else control structure. There is no transaction in the OUTPUT module because there is only one action named outJob_ass. In the INTERNAL_TRANSITION module, the tr2 transaction calls the sigma_ac2 action and the phase_ac2 action with the sequential control structure. After simulation, we can get the model instance for the processor model.

The initial states and input sets have been set before simulation. The initial phase is passive, the initial time advance is infinite and the initial value of the job variable is 0. There are three input events in the time points of 1 and 4. The elemental detail tables of this model instance are shown in Figure 10.

There are several benefits for the structured representation of DEVS models. The modular tree and genus graph can represent an atomic model in a visual format. If the modeler does not use some variable in a newly developed model, the variable can be deleted and the actions and transaction calling the variable will be deleted automatically in the genus graph. The model instance shows the simulation results in detail. The modeler can choose corresponding tables to look at the state updates, input events or output events.
4.2 The Structured Representation of an Coupled Model

We have defined the coupled model class based on ESM. In this section, we use the model class to represent the EFP (Experiment Frame and Processor) model, as shown in Figure 11. The EFP model is a coupled DEVS model and has three components: Generator, Processor and Transducer. Each component associates with an atomic DEVS model. For example, Generator is a component of the genr model.

The EFP coupled model is represented as a model instance of the coupled model class. The elemental detail tables for this model instance are shown in Figure 12. The model elements of the EFP model are all listed in these tables.

5 CONCLUSION

In this paper, we use the ESM to represent a DEVS model as a structured model. For the atomic DEVS formalism, we propose an atomic DEVS model template represented as an ESM model schema. This template contains basic modules and genera for the representation of various specific atomic models. The model instance for an atomic model is to represent the simulation behavior in some initial states and input sets. For the coupled DEVS formalism, we propose a coupled model class represented as an ESM model schema. A specific coupled model is a model instance of this model schema.

The structured representation of DEVS models provides a visual format to build and maintain models. An atomic model can be represented as a modular tree or a genus graph. The modeler can add or delete model elements visually. Furthermore, the simulation behavior of an atomic model is represented as elemental detail tables in detail. The user can observe the simulation results conveniently. The model elements of a coupled model are stored in a series of tables, where they can be edited clearly.

We are planning to develop a DEVS modeling platform to support the ESM model representation. A DEVS model is firstly represented as an ESM model, and then is transformed into a model executed by CD++ or DEVSJava. The DEVS models are created and maintained in a structured format in this platform. The Java language and the data base technology are used in developing this platform.
Table Name: MODEL

| MODEL || INTERP |
|-------|--------|
| genr  | a generator atomic model |
| proc  | a processor atomic model |
| transd| a transducer atomic model |

Table Name: INPUT_PORT

| INPUT_PORT || INTERP |
|------------|--------|
| inStop     | stop job port |
| inJob      | in job port |
| inArriv    | arrive job port |
| inSolved   | solved message port |

Table Name: MODEL_INPUT_PORT

<table>
<thead>
<tr>
<th>MODEL</th>
<th>INPUT_PORT</th>
</tr>
</thead>
<tbody>
<tr>
<td>genr</td>
<td>inStop</td>
</tr>
<tr>
<td>proc</td>
<td>inJob</td>
</tr>
<tr>
<td>transd</td>
<td>inArriv</td>
</tr>
</tbody>
</table>

Table Name: COMPONENT

| COMPONENT || INTERP |
|-----------|--------|
| Generator | a genr component |
| Processor | a proc component |
| Transducer| a transd component |

Table Name: IC

<table>
<thead>
<tr>
<th>COMPONENT</th>
<th>OUTPUT_PORT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Generator</td>
<td>outJobG</td>
</tr>
<tr>
<td>Generator</td>
<td>outJobP</td>
</tr>
<tr>
<td>Processor</td>
<td>inJob</td>
</tr>
</tbody>
</table>

Table Name: MODEL_OUTPUT_PORT

<table>
<thead>
<tr>
<th>MODEL</th>
<th>OUTPUT_PORT</th>
</tr>
</thead>
<tbody>
<tr>
<td>genr</td>
<td>outJobG</td>
</tr>
<tr>
<td>proc</td>
<td>outJobP</td>
</tr>
<tr>
<td>transd</td>
<td>outStop</td>
</tr>
</tbody>
</table>

Table Name: MODEL_COMPONENT

<table>
<thead>
<tr>
<th>MODEL</th>
<th>COMPONENT</th>
</tr>
</thead>
<tbody>
<tr>
<td>genr</td>
<td>Generator</td>
</tr>
<tr>
<td>proc</td>
<td>Processor</td>
</tr>
<tr>
<td>transd</td>
<td>Transducer</td>
</tr>
</tbody>
</table>

Table Name: SEL

<table>
<thead>
<tr>
<th>COMPONENT</th>
<th>COMPONENT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Generator</td>
<td>Processor</td>
</tr>
<tr>
<td>Generator</td>
<td>Transducer</td>
</tr>
</tbody>
</table>

Figure 12: Elemental detail tables for the EFP model

ACKNOWLEDGEMENTS

The financial supports from the National High Technology R&D programs of China (2012BAE05B03 & 2013AA040701) are gratefully acknowledged.

REFERENCES


AUTHOR BIOGRAPHIES

Yunping Hu is a doctor candidate of Control Science and Engineering at Zhejiang University in Hangzhou, China. He holds a M.S. in Control Science and Engineering from Nanjing University of Technology in Nanjing, China. His research interests include modeling & simulation, model management and decision support system. His email address is yphu@iipc.zju.edu.cn.

Jun Xiao is a master candidate of Control Science and Engineering at Zhejiang University in Hangzhou, China. He earned his Bachelor’s degree in Automation from Wuhan University in Wuhan, China. His research interests include DEVS modeling & simulation and mathematical knowledge management. His email address is jxiao@iipc.zju.edu.cn.

Gang Rong is Professor of Control Science and Engineering at Zhejiang University in Hangzhou, China. His research interests cover modeling, simulation & optimization, data-ming, data visualization and enterprise-control system integration in process industries. He holds a Ph.D. in Control Science and Engineering from Zhejiang University in Hangzhou, China. His email address is grong@iipc.zju.edu.cn and his web page is http://mypage.zju.edu.cn/en/rglab.

Xiaolin Hu is an Associate Professor in the Computer Science Department at Georgia State University, Atlanta, Georgia. He received his PhD degree from the University of Arizona, MS degree from the Chinese Academy of Sciences, and BS degree from the Beijing Institute of Technology in 2004, 1999, and 1996, respectively. His research interests include modeling and simulation theory and application, agent and multi-agent systems, and complex systems science. He has served as program chairs for several international conferences/symposiums in the field of modeling and simulation, and is an associate editor for Simulation: Transaction of The Society for Modeling and Simulation International. Dr Hu is a National Science Foundation (NSF) CAREER Award recipient. His email address is xhu@cs.gsu.edu.